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The screenshot displays the HyperLynx LineSim v8.2.1 interface. The main window shows a schematic diagram of a USB link simulation. On the left, a 'Host' component is connected to a network of transmission lines (TL1, TL2, TL33, TL34, TL36, TL37) and a ribbon cable. On the right, a 'Device' component is connected to another network of transmission lines (TL9, TL10, TL11, TL12) and a 5-meter USB cable. The diagram includes various routing types such as Host Routing, Ribbon Cable, Centri Panel Routing, and Peripheral Routing. Two resistors, R7 and R8, are shown at the bottom with a value of 1.0 M ohms. An 'About' dialog box is open in the bottom right corner, providing system information.

**About Dialog Box Content:**

- LineSim v8.2.1** [v8.2 build 269387]  
GBS Install Id: 27413  
Jul 26 2012
- Operating system in use:**  
Windows 7 V 6.1, Build 7601, Service Pack 1  
Compatible with Windows 7, Vista, and XP
- Memory status:**  
Physical memory in use: 192 MB (82 MB RAM, 110 MB pagefile(s))  
Physical memory accessible: 4095 MB  
Address space in use: 284 MB  
Address space accessible: 4095 MB
- Program Location:**  
C:\MentorGraphics\9.5PADSVSDD\_HOME\hyperlynx\
- Copyright 2012 Mentor Graphics Corporation. All Rights Reserved.  
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